

DEVICE FOR EVALUATING RELATIVE VALUE FOR PART OF DATA SIGNAL IN COMMUNICATION SYSTEM

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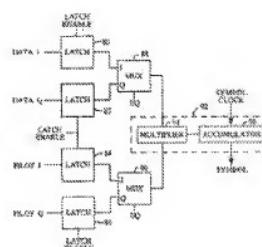
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Abstract of RU 2202154 (C2)

FIELD: calculations of scalar projection of vector onto other vector. SUBSTANCE: device has multiplying circuit and adding circuit. Multiplying circuit functions to multiply value presenting first component of first vector by value presenting first component of second vector so as to obtain first intermediate value and to multiply value presenting second component of first vector by value presenting second component of second vector so as to obtain second intermediate value. Adding circuit functions to add first intermediate value to second one so as to obtain resultant value presenting scalar projection of first vector onto second vector. Device may have in addition first memory circuit, first selection circuit, second memory circuit, and second selection circuit. First memory circuit functions to store values presenting first and second components of first vector and second memory circuit is used to store values presenting first and second components of second vector. Selection circuits provide for alternate supply of these values to multiplying circuit. EFFECT: provision for weighing signal relative to received reference signal when demodulating modulated signal. 12 cl, 6 dwg



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